Section 6.4

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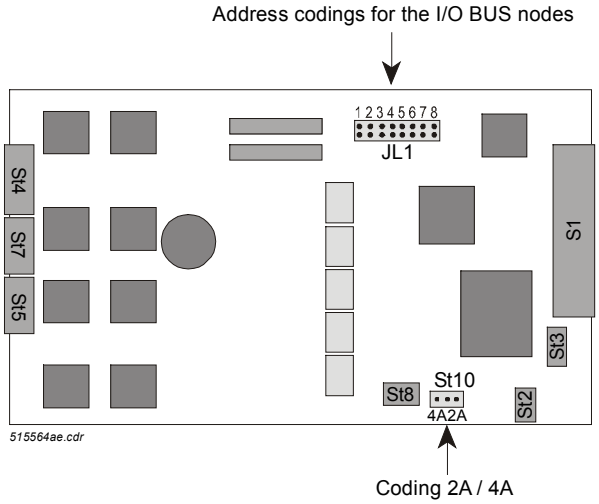
1 I / O BUS Nodes Digitizer

Standard settings valid for the ADC Solo.

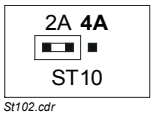
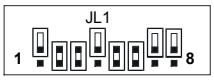
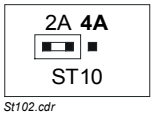
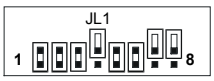
Excample:

Slow-Scan-BD CM+8.9499.8100.0

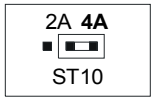
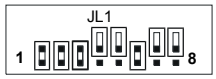


In case of replacement of a PCB take care of the settings listed below.

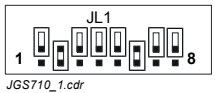
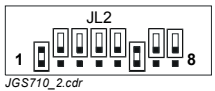
Scanner (500):

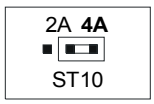
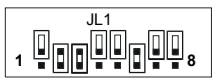
GS	Designation	IO-Bus Address	Switch	Position
412	IP-Slow-Scand-BD	36H	ST 10 Motor Current 2A JL1 Address setting	 St102.cdr  JLGS412.cdr
410	Scan-Rolls-Lift-BD	37H	ST 10 Motor Current 2A JL1 Address setting	 St102.cdr  LJGS410.cdr

Transport (600):

GS	Designation	IO-Bus Adresse	Switch	Position
602	IP-Transport-BD	27H	ST 10 Motor Current 4A JL1 Address setting	 St10.cdr  JLGS602.cdr

Cassette Unit (700)

GS	Designation	IO-Bus Adresse	Switch	Position
710	5Fold-Step-BD	22H 21H	JL1 Address setting for clamp. Belt and lift-motor JL2 Address setting for opener and roll-motor	 JGS710_1.cdr  JGS710_2.cdr

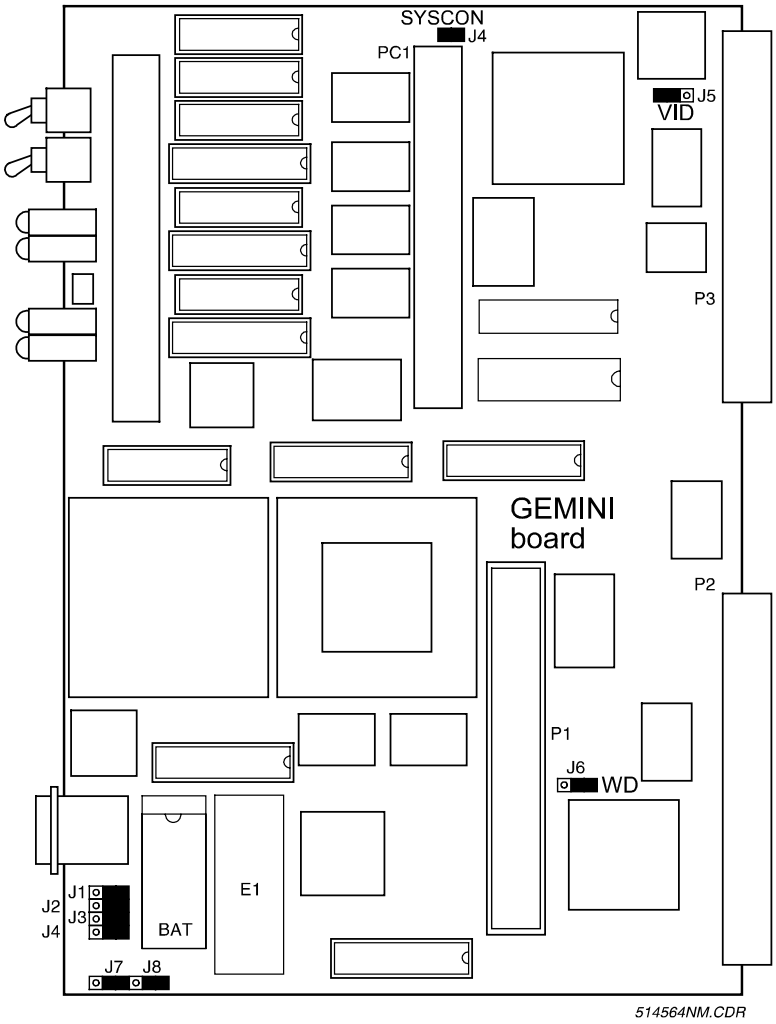
GS	Designation	IO-Bus Adresse	Switch	Position
712	IP-Alignment-BD	26H	ST 10 Motor Current 2A JL1 Address setting	 St10.cdr  JLGS712..cdr

2 Fuses

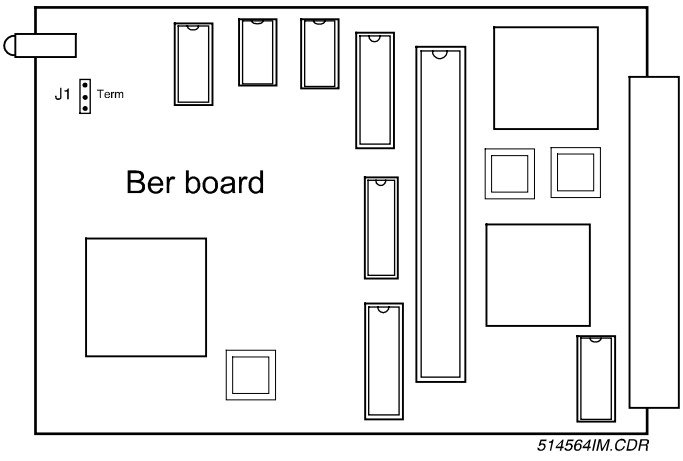
Location	Fuse for		
Power Integration-BD	GS410	Si1	3,2A slowblow
	GS602	Si2	3,2A slowblow
	GS712	Si3	3,2A slowblow
	GS412	Si4	3,2A slowblow
	GS710	Si5	6,25A slowblow
Power Unit	GS240	Si 201	2,0A slowblow
Step motor transformer	TR206	Si202	2,0A slowblow
VME-Adapter- BD	IO-Bus	SI1	3,2A slowblow

3 VME Boards Digitizer

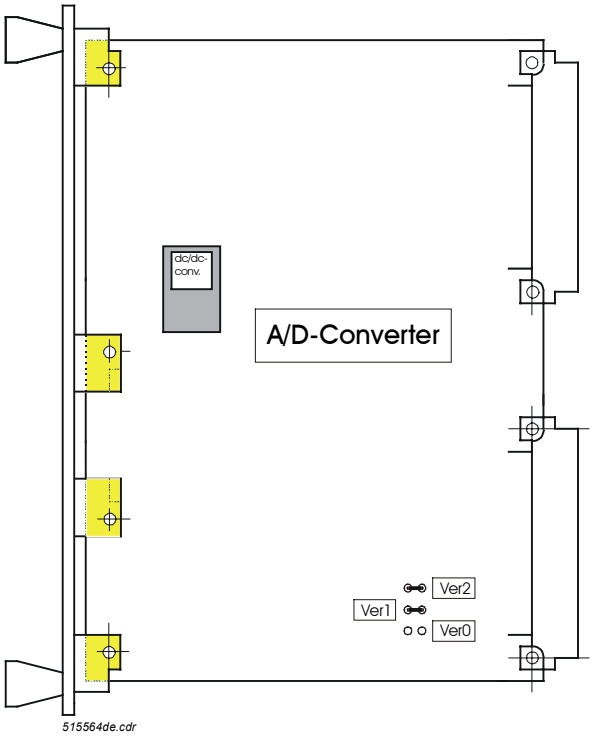
3.1 Gemini Board



3.2 BER board



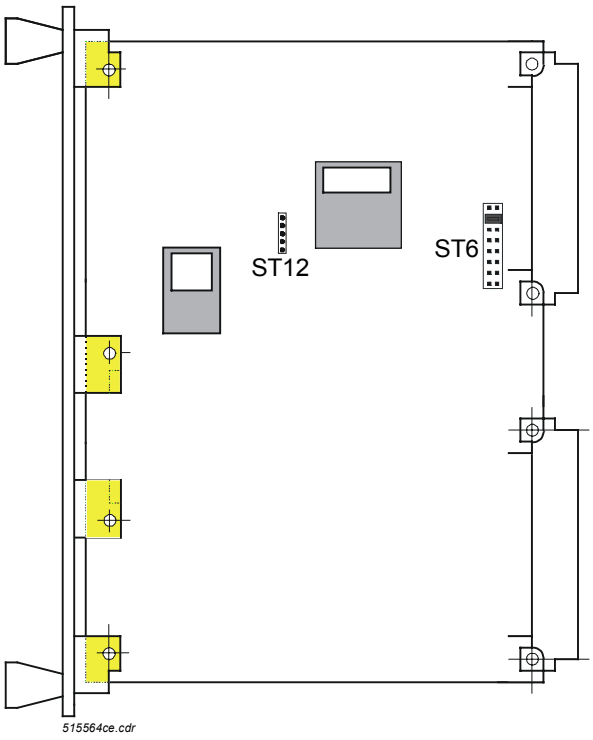
3.3 SAB board (Scan Aquisition Board)



Function of the jumpers:

The bridges VER1 and VER2 are fixed now.

3.4 SCB board (Scan Control Board)

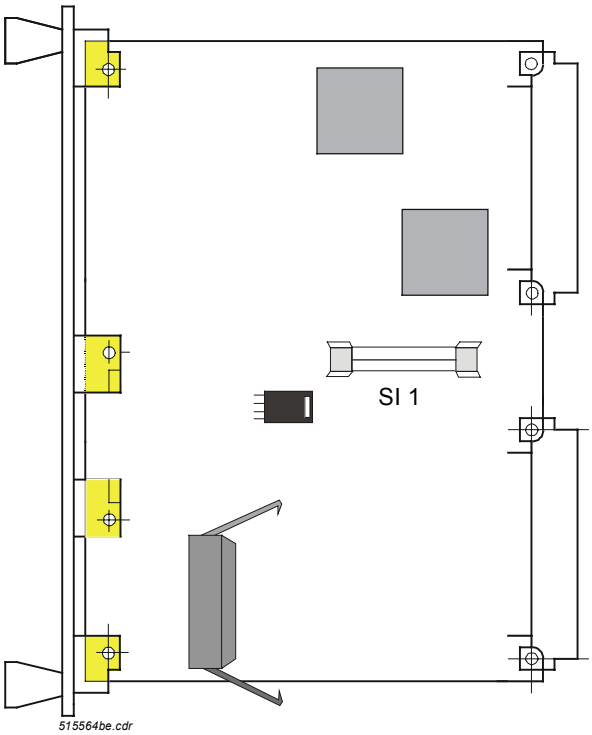


Functions of the jumpers:

ST6: connects the interrupt generated on the board to VME interrupt line IRQ6 and has to be closed as shown in the drawing

ST12: This jumper row is forseen for boundary scan connection, for testing of the FPGA. In normal operation mode this jumper row is not connected.

3.5 VME-Adapter-board



- Mind fuse SI 1 (IO-bus)